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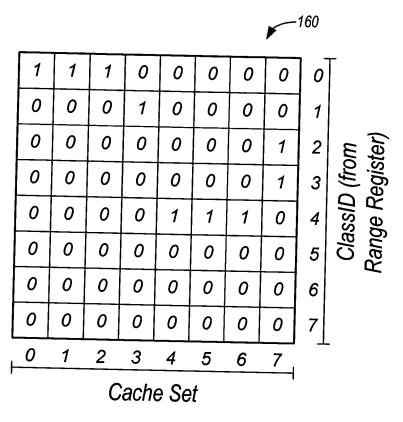


FIG. 2

Replacement Management Table:				
RMT_index	RMT_entry			
RMT_index 0	Set Enable Bits	V	b	а
RMT_index n	Set Enable Bits	V	b	а

FIG. 3

Range Start Register (RSR)	400
Range Start Register (high):	
0	31
Upper 32-bits of the starting address	
Range Start Register (low):	
32	63
Lower 20-bits of the starting address 0 0 0 0 0 0 0	0 0 0 0 R V
FIG. 4	
Range Mask Register	200
Range Mask Register:	
32	63
Lower 20-bits of the starting address 0 0 0 0 0 0	0 0 0 0 0 0
FIG. 5A	
ClassID Register (CIDR)	
Class IDs:	
0	

FIG. 5B

RclassID

